

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: MICHAEL J. SCHAFER

Serial No.: 10/046,596

Filed: October 22, 2001

For: Providing Integrated Chassis Antenna
For Processor-Based Devices

§ Group Art Unit:

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Examiner:

Atty. Dkt. No.: ITL.0588US (P11729)



REQUEST FOR COPY OF THE OFFICIAL FILING RECEIPT

Commissioner of Patents
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Dear Sir:

The above-referenced application was filed on October 22, 2001. Applicant has not received the Official Filing Receipt.

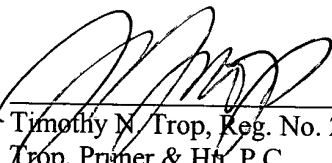
Applicant requests a copy of the Official Filing Receipt be sent to the attorney of record at:

Timothy N. Trop
Trop, Pruner & Hu, P.C.
8554 Katy Freeway, Ste. 100
Houston, TX 77024.

Any fees required by this request should be charged to Deposit Account No. 20-1504 (ITL.0588US) and this paper is submitted in duplicate.

Respectfully submitted,

Date: March 27, 2002


Timothy N. Trop, Reg. No. 28,994
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Date of Deposit: March 27, 2002

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, Washington DC 20231.


Sherry Tipton



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Bib Data Sheet

CONFIRMATION NO. 2881

SERIAL NUMBER 10/046,596	FILING DATE 10/23/2001 RULE	CLASS 455	GROUP ART UNIT 2681	ATTORNEY DOCKET NO. INTL-0588-US
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APPLICANTS

Michael J. Schaffer, Beaverton, OR;

** CONTINUING DATA *****

N/A

** FOREIGN APPLICATIONS *****

N/A

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/02/2002

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY OR	SHEETS DRAWING 9	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature: <i>Michael J. Schaffer</i> Initials: <i>TDC</i>				

ADDRESS
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 8554 KATY FWY, STE 100
 HOUSTON, TX 77024-1805
TITLE

Providing integrated chassis antenna for processor-based devices

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